

5 The test functions define the attributes of the CPU read/write paths 18, 20 and include:

Please replace the paragraph beginning at line 13 of page 11 as shown.

word offset – defining an offset location of the register in memory

bit offset - defining the bit location of each element in the register and derivable from the bit length BL in the data structure

6 bit field – naming the element of that bit location

function – defining the function F of the element

reset state – value of entity on reset

read/write – whether entity is readable or writable from CPU

Please replace the paragraph beginning at line 12 of page 12 as shown.

7 Figure 5 is a flow chart illustrating high level operation of the modelling tool 24. At step S1, input parameters given to the modelling tool 24. At step S1, input parameters give to the modelling tool are checked. At step S2, the input file corresponding to one of the specified parameters is opened. Its contents are checked and any errors are reported in a meaningful manner (step S3) by an error routine. If the contents of the input file are valid, the files are opened and named at step S4. At step S5, the files are created as described earlier with reference to Figures 3 and 4. Finally, at step S6, the files are closed.

### IN THE CLAIMS

Please rewrite the claim 5 as shown.

8 5. (Amended) A computer system which comprises a processor and a memory, the memory holding a program representing a modelling tool for use in designing an application specific processor (ASP), wherein the computer system comprises an input means for receiving a plurality of input files, each input file defining the functional attributes of a peripheral for the ASP in a high level language within an input data structure;

9 the processor being operable to execute the program representing the modelling tool to generate from the input file a register definition file by allocating specific elements of the input data structure to predefined sectors of a register definition table; and

10 wherein the computer system further comprises an output means for outputting the register definition file in a manner which is usable to create in silicon the registers of the ASP.